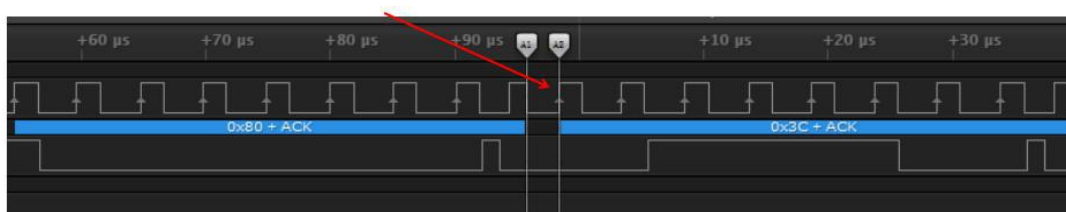


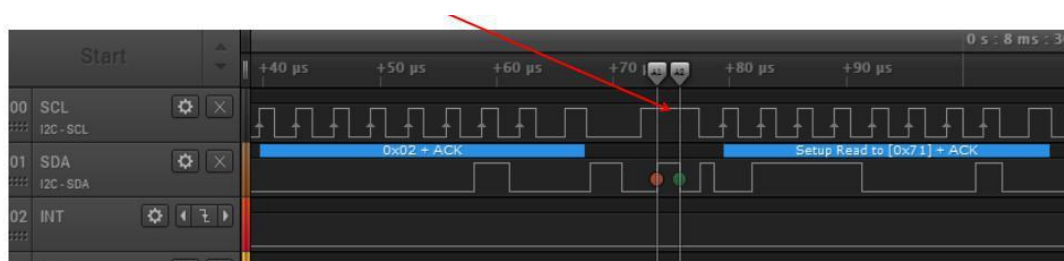
HY46XX I2C 通讯时序要求

HY46XX I2C communication timing requirements

1. I2C 通讯时钟建议 200KHz，上拉电阻 $2.2k\sim 4.7k\Omega$ 。（The I2C communication clock is recommended less than 200KHz, and the pull-up resistor is $2.2k\sim 4.7k\Omega$.)
2. 字节与字节之间延时需大于 30us，如下图红色箭头位置的时间（The delay between bytes and bytes needs to be more than 35us, For example, the time delay of the red arrow position in the following figure:）



3. STOP 到 START 信号之间延时需大于 30us，如下图红色箭头位置的时间。（STOP and then to the next START signal need to be more than 30us, For example, the time delay of the red arrow position in the following figure.）



4. 如果使用 RESTART 方式，RESTART 和前面一个字节之间延时需大于 30us，如下图红色箭头位置的时间。（If RESTART is used, the RESTART must be delay more than 30us between the preceding byte, For example, the time delay of the red arrow position in the following figure.）

